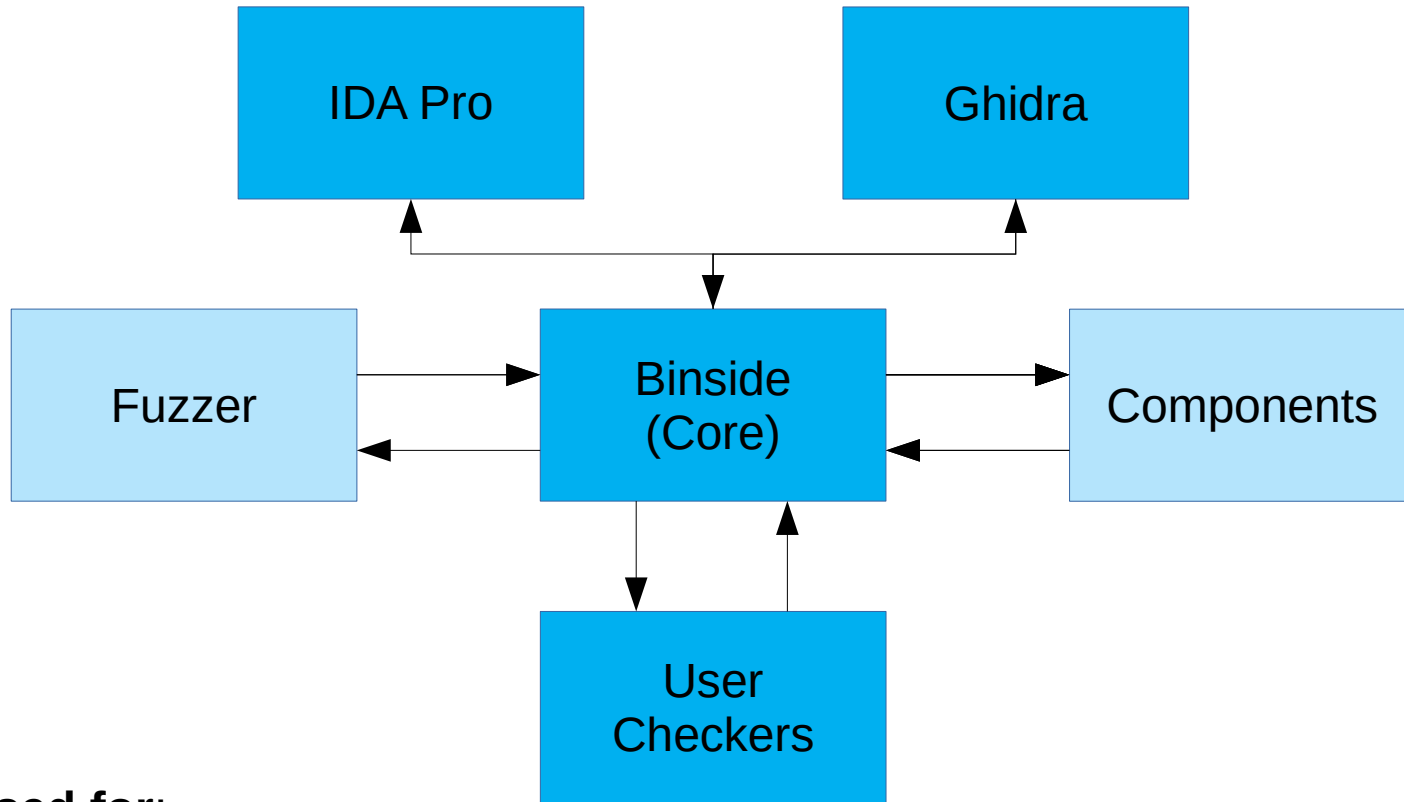


BinSide: Binary static analysis framework

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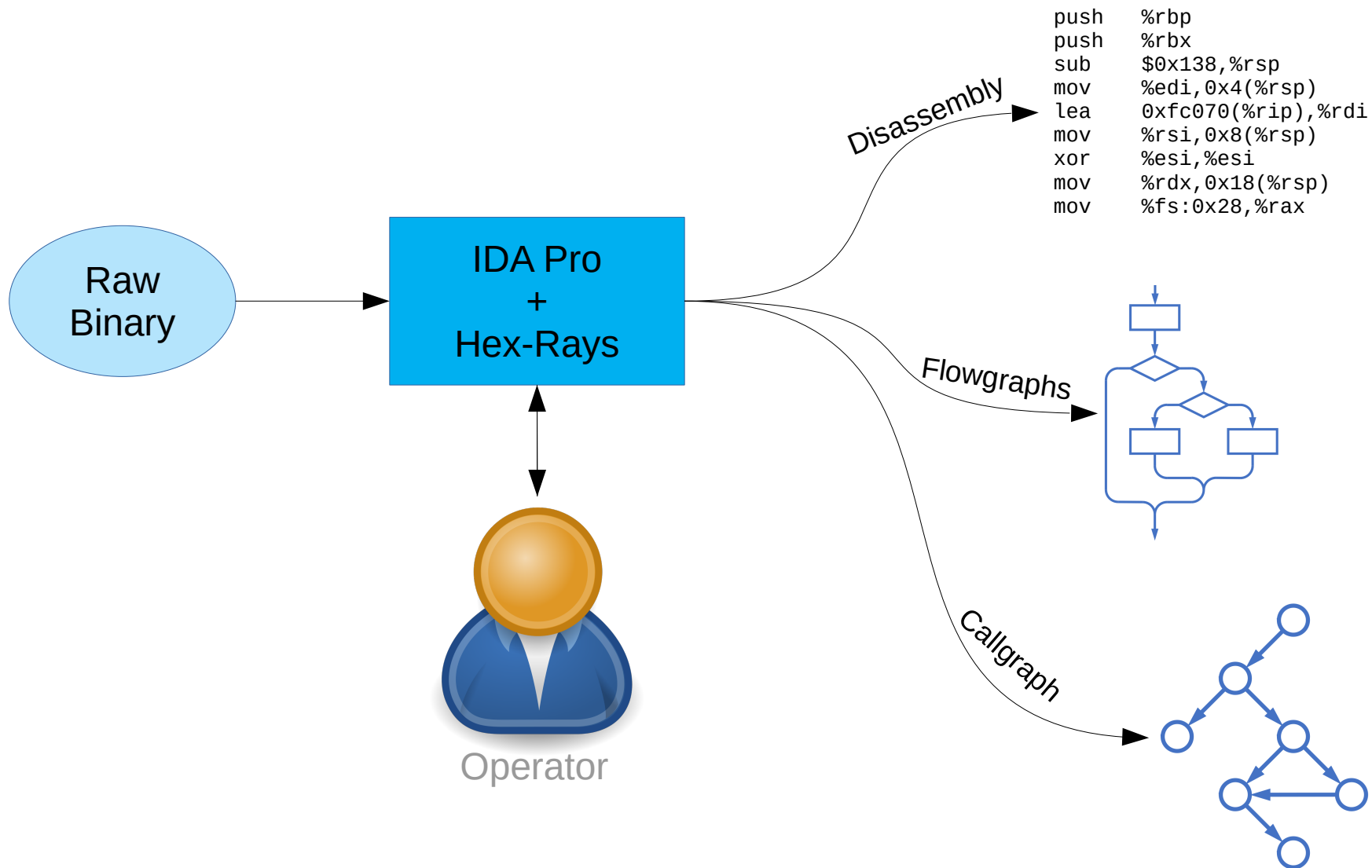
BinSide: Architecture



Can be used for:

- Interprocedural defects search
- Binary code analysis acceleration
- Dynamic analysis assistance

BinSide: IDA Pro



BinSide: Intermediate representation REIL*

*REIL - Reverse Engineering Intermediate Language

```
push %rbp
push %rbx
sub $0x138,%rsp
mov %edi,0x4(%rsp)
lea 0xfc070(%rip),%rdi
mov %rsi,0x8(%rsp)
xor %esi,%esi
mov %rdx,0x18(%rsp)
mov %fs:0x28,%rax
```

Disassembly

BinSide
(REIL translators)

REIL

```
# mov %edi,0x4(%rsp)
and rdi, 0xffffffff, t1
add 4, rsp, t4
and t4, 0xffffffffffffffff, t5
add t5, ssbase, t7
stm t1, EMPTY, t7
# lea 0xfc070(%rip),%rdi
str 1223776, EMPTY, rdi
# mov %rsi,0x8(%rsp)
str rsi, EMPTY, t0
add 8, rsp, t3
and t3, 0xffffffffffffffff, t4
add t4, ssbase, t6
stm t0, EMPTY, t6
```

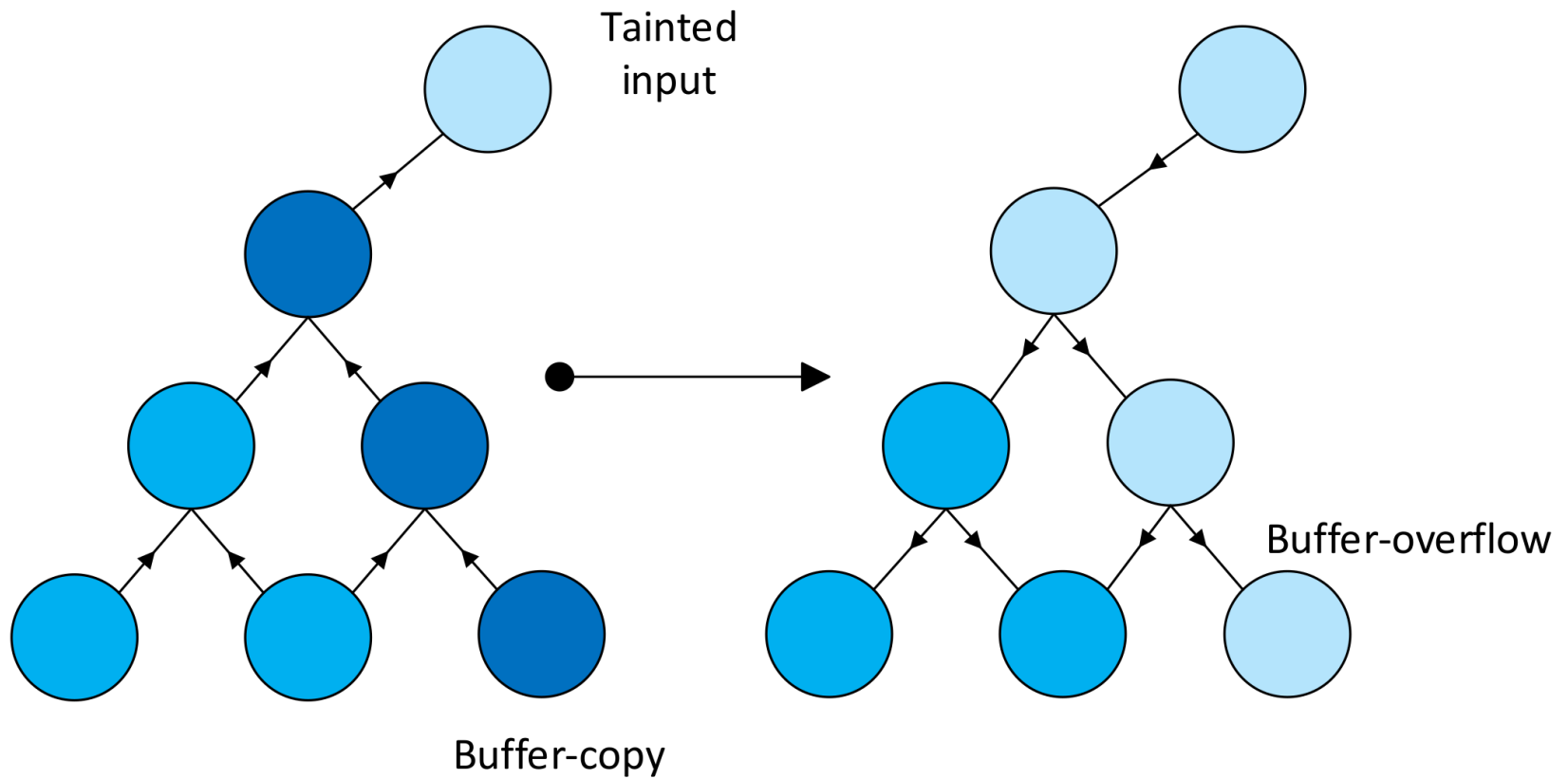
Translators supports 4 architectures:

- ARM
- MIPS
- x86
- x64

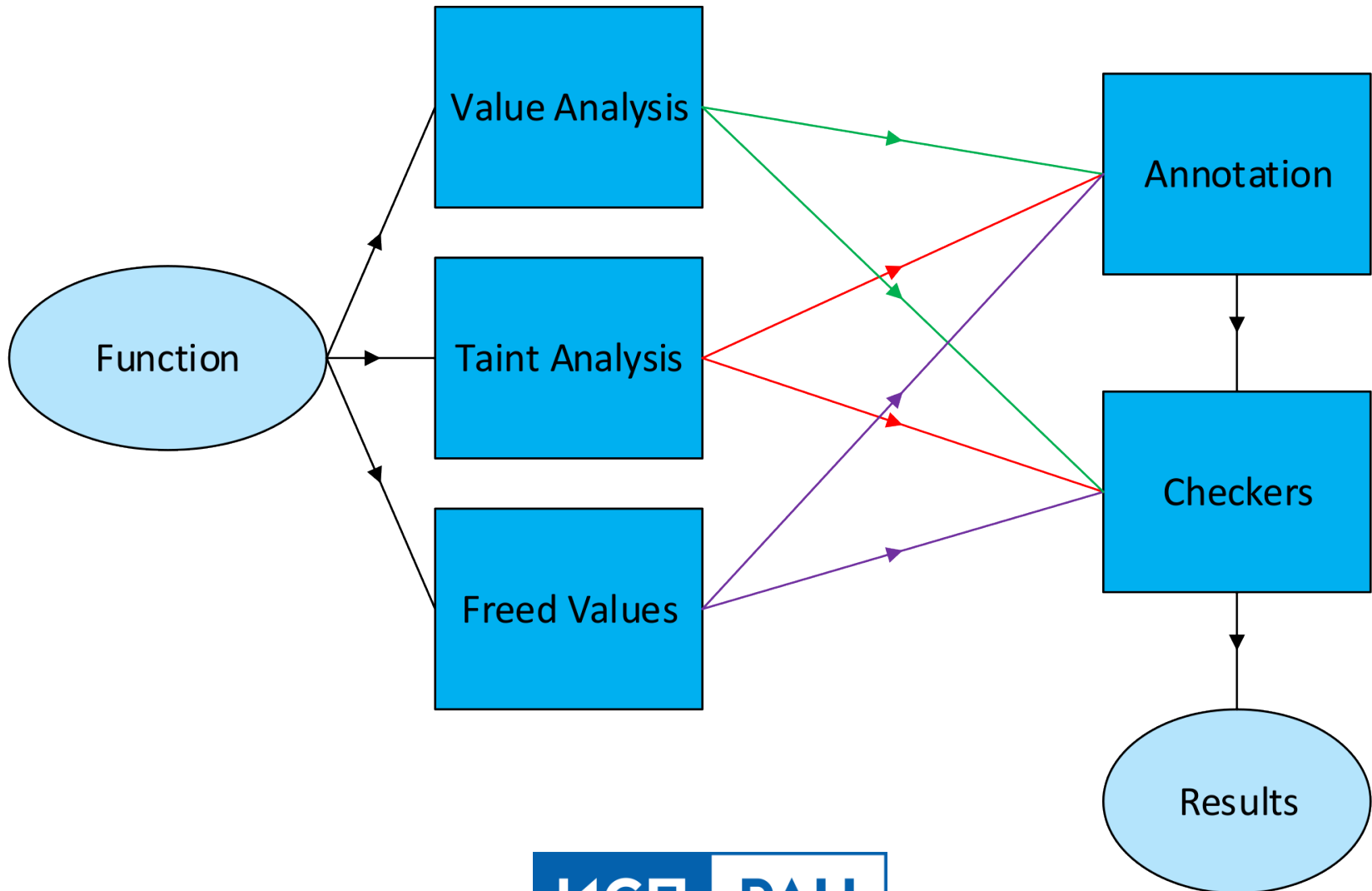
REIL consists of 17 non-side-effects instructions:

- 10 for math and logic operations
- 1 for conditional jump
- 3 for data transfer operations
- 3 for service instructions

BinSide (Core): Interprocedural defects search



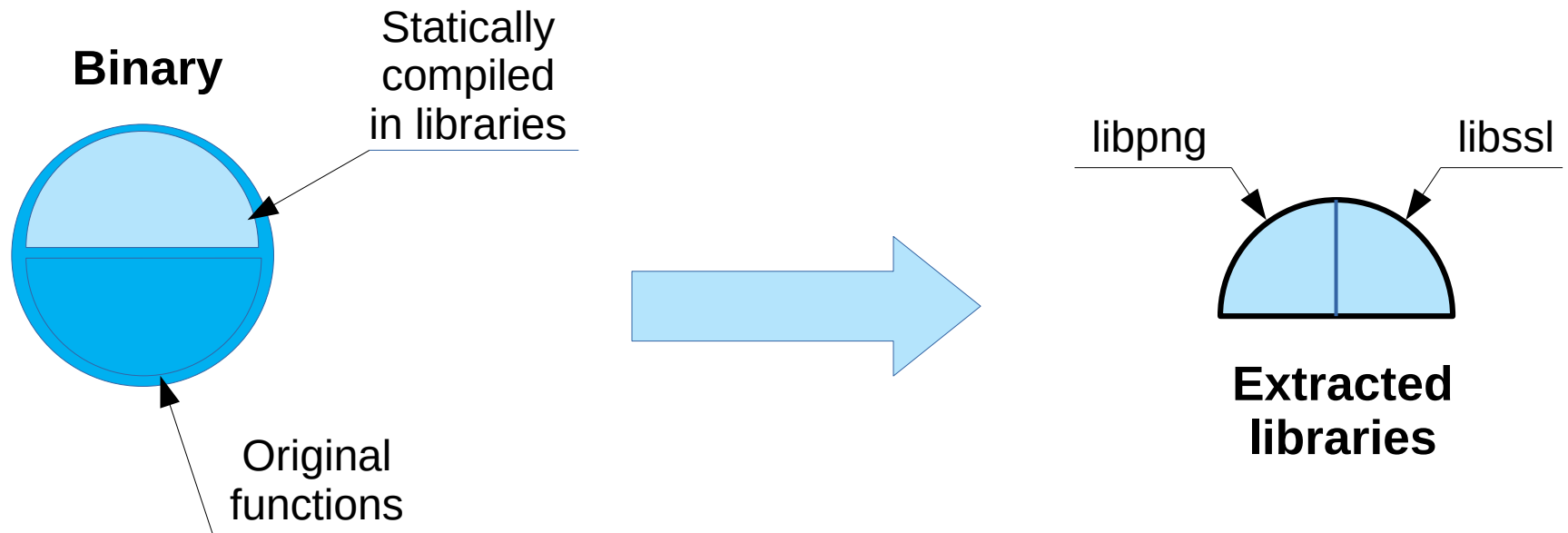
BinSide (Core): Intraprocedural analysis



BinSide: Types of searched defects

- Heap memory allocation-deallocation issues (CWE-415, CWE-416)
- Inaccurate data copy operations (CWE-121, CWE-122)
- Use of externally-controlled format string (CWE-415)

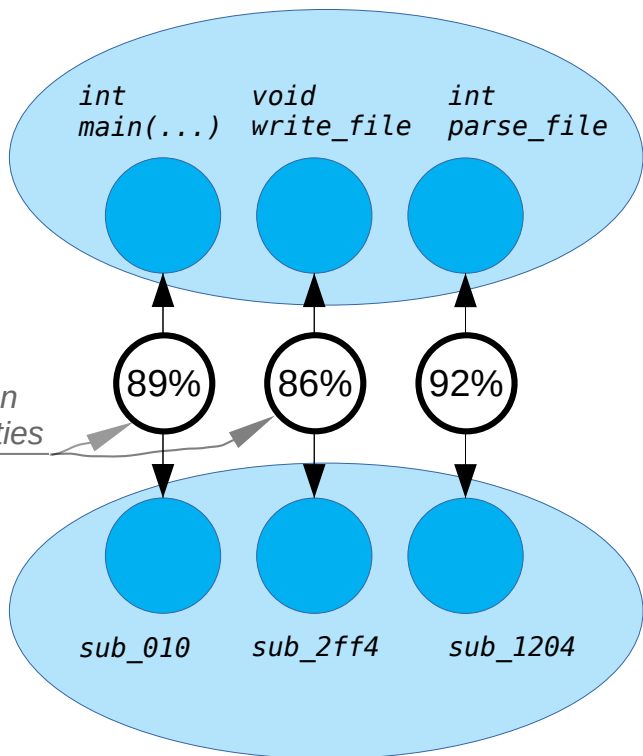
BinSide: Components (*Library Identification)



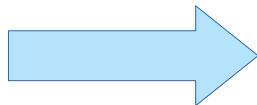
*Based on Binary Code Clone Search Tool

BinSide: Components (*Function Name Recovery)

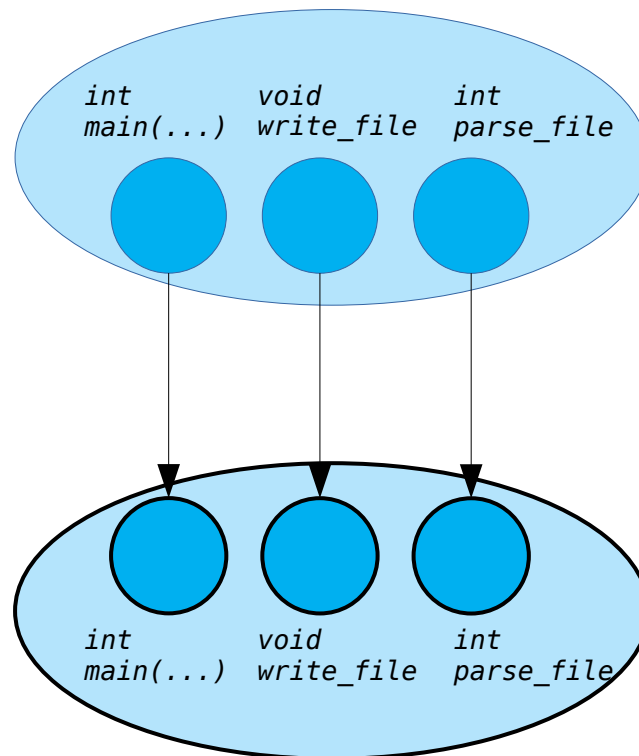
Binary with known functions



Binary with unknown functions



Binary with known functions

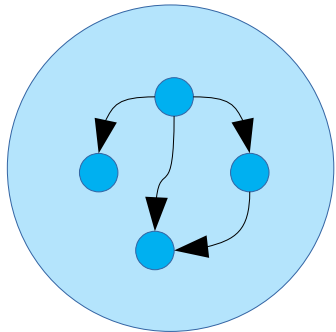


Binary with recovered function names

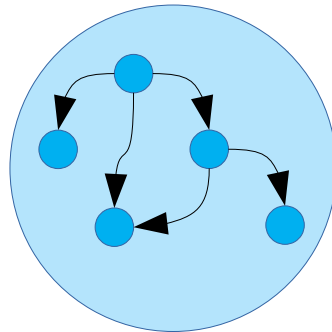
*Based on Binary Code Clone Search Tool

BinSide: Components (*Binary Differences between releases)

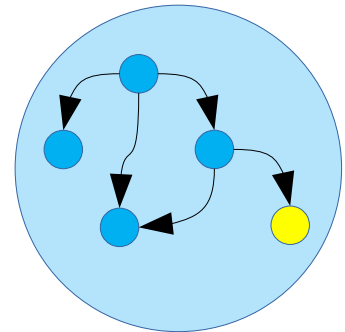
Old binary



New binary



New binary



*Based on Binary Code Clone Search Tool



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